

Job Detail

Staff Level

Position Title	[Osaka] LSI digital/analog design engineer/Stable career advancement possible!
Company Name	SEEDEA Corporation Osaka Design Center
Activated / Updated	2024-04-11 / 2024-04-11
Job Type	Electronics (Appliance/Semiconductor) - Design/CAD Design/CAD Operator Electronics (Appliance/Semiconductor) - Valuation/Testing Electronics (Appliance/Semiconductor) - Project/Production Manager
Industry	Outsourcing
Location	Asia Japan Osaka
Job Description	<p>■Attractive points■ ①Japan's ever-growing semiconductor industry★ ② Place of assignment will be determined taking into account wishes and experience ③ Multinational employees are active!</p> <p>■Recruitment background■ Due to strong business performance, we are looking for core members to expand our business and launch a new team!</p> <p>■Business details■ [LSI analog/digital circuit design] In the development of semiconductors (digital/analog LSI) Design, testing and evaluation work</p> <p>Depending on your experience, <ul style="list-style-type: none"> • Front end design • Middle end design • Backend design • Test/Evaluation I'll leave it to you!</p> <p>[Development example] <ul style="list-style-type: none"> • CMOS image sensor • High-speed communication interface IP • Power IC • Memory (Flash, SRAM, DRAM) Such</p> <p>■Career path■ ④ Improve your skills with long-term projects! Our top priority is to entrust work that will improve the skills of each engineer. Because we have built long-term relationships of trust with our customers, We provide an environment where you can steadily build your career through projects. In the future, you can also work in PM/PL class!</p>
Company Info	<p>We respect your desire to grow. Since we have a business office in the Philippines, we have an environment where you can work regardless of your nationality.</p> <p>[Prevention of passive smoking] No smoking indoors (smoking room available)</p>
Working Hours	<p>9:00~18:00 Holidays: Saturdays, Sundays, holidays, and long holidays *Depends on the assigned project *Average overtime around 20 hours</p>
	<p>■Required conditions■ <ul style="list-style-type: none"> • Knowledge of digital or analog circuit design • Japanese language ability above daily conversation level <p>■Welcome conditions■1 year or more of the following experience <ul style="list-style-type: none"> • Experience in analog circuit/layout design </p> </p>

Qualifications	<ul style="list-style-type: none"> · Experience in digital middle to backend design <p>■The person you are looking for■</p> <ul style="list-style-type: none"> ·communication ability · High desire to grow · Flexibility to suit the environment
English Level	Business Conversation Level (TOEIC 735-860)
Japanese Level	Business Level(JLPT Level 2 or N2)
Chinese Level	None
Salary	JPY - Japanese Yen JPY 3500K - JPY 8000K
Salary Description	<p>We will decide based on your experience and skills.</p> <p>Actual transportation expenses paid (up to 30,000 yen)</p> <p>Rented company housing system available (movement conditions apply)</p>
Holiday Description	<p>Complete 2-day week (Saturdays, Sundays, and public holidays)</p> <p>New Year holiday paid holiday</p> <p>Congratulatory and condolence leave</p>
Job Contract Period	Full-time employment *Contract employee during trial period (6 months), then consideration for promotion to permanent employee
Nearest Station	JR/Osaka Metro Midosuji Line Shin-Osaka Station 5 minutes walk